

AMENDMENTS TO THE SPECIFICATION

Please replace the first full paragraph of page 9 with the following amended paragraph:

Fig. 2 is a circuit diagram showing a specific configuration of the ground-fault detecting device 1 of Fig. 1. As shown in Fig. 2, the pulse signal generating unit 31 of the ground-fault detecting device 1 is a rectangular wave oscillation circuit which includes inverters IC1 and IC2, resistors R01, R02, and R03, and a capacitor C01 and produces a rectangular pulse signal Vosc that has a constant frequency and a duty cycle of 50% and in which a high level and a low level appear repeatedly in a prescribed cycle T1. The rectangular pulse signal Vosc that is output from the rectangular wave oscillation circuit is supplied to the connecting point of the detection resistor R04 and the coupling capacitor Co and appears there as a detection voltage Vx. The detection resistor R04 serves as a part of a voltage divider that is formed by the detection resistor R04 and a vehicle-side insulation resistance RL, and divides the detection voltage Vx at the occurrence of a ~~round~~ground fault.